**PCB CHECKLIST\_ REV. 8**

Checklist for the PCB: \_DER-635\_REVD-QFN\_\_\_\_\_\_\_

Design by: \_\_Guy Clark/ Noam Ezra\_\_\_\_\_\_\_\_\_\_\_\_\_

PI Logo 

Add Family name example: LinkSwitchTM-PH for InnoSwitch TM-CH,CV,RC,MK,EP.

Board name RD-xxx or DER-635

Revision Rev D

Date code 1824 (year week)

Serial number S/N

Correct Rev. (of PCB design) being sent out.

Primary to Secondary safety isolation has been met. (for 6.8mm spacing) (6.25 specified for this design)

Add SST for Jumper that uses the TOP layer trace.

Copper pour and make certain that it is attached to the net list.

AC input connector add silk screen L for line, and N for Neutral, PE for Protective Earth.

Output connector add silk screen + (Voltage) V and RTN or Vo and 0v

Transformer Flying Lead add silk screen FL(x)…FL(y)

Solder mask on spark gap in Rectangle shape (6.4mm spacing) on the corrected layer.

Added note to show the slot location using Drill Drawing Layer.

Board Dimension.

Board Material, Copper, thickness.

No silkscreen on copper pads

Component Placement File created.

Special Instruction(s) met:

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Responsible Engineers Approval (Type name) \_Noam Ezra\_\_\_\_\_\_\_\_\_\_\_\_\_

**For Green Board Fabrication (Need Approval from Kevin)**

Approved by \_Noam Ezra\_\_\_\_\_\_\_

Date \_\_\_ \_\_\_